Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	258908	(382/128,129,130,131,132,133, 134).CCLS. or (("600") or ("378") or ("250") or ("128")).CLAS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 15:23
L2	547	1 and sag	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:08
L3	41	2 and ((align$4 or adjust$4) near4 (imag$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:08
L4	23	3 and compensat$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 15:29
L5	1	4 and (slice near4 image)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 15:29
L6	3	3 and (slice near4 image)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 15:41
L7	2	("5995581").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 15:41
L8	2	7 and position$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:08

L9	1	8 and (slice near4 image)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 15:41
L10	0	("9and(calculat$4ormeasur$4orcomput$4)").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 15:42
L11	1	9 and (calculat$4 or comput$4 or measur$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:08
L12	0	11 and sag	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 15:42
L13	35	sag and (slice near4 imag$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:16
L14	22	1 and 13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:04
L15	7	14 and ((align$4 or adjust$4) near4 (imag$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:46
L16	5	15 and (calculat$4 or comput$4 or measur$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:17

L17	5	16 and sag	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:08
L18	5	17 and position$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:17
L19	5	18 and (slice)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:09
L20	445	(swag or bend$4 or sag) and (slice near4 imag$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:23
L21	231	1 and 20	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:17
L22	59	21 and ((align$4 or adjust$4) near4 (imag$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:17
L23	56	22 and (calculat$4 or comput$4 or measur$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:17
L24	56	23 and position$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:18

L25	47	24 and (accumulat$4 or collect$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:18
L26	4	((swag or bend$4 or sag) near4 (correct$4)) and (slice near4 imag$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:28
L27	2	26 and bend$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:29
L28	2	27 and tabl$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:29
L29	2	28 and stor$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:30
L30	0	29 and (acquair$4 near4 imag$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:30
L31	1	29 and (acquir$4 near4 imag$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:31
L32	0	31 and (imag$4 near4 position)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:31

L33	1	31 and (imag$4 near4 position$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:38
L34	1	33 and stor$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:46
L35	1	34 and adjustment$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:47
L36	0	35 and ((align$4 or adjust$4) near4 (imag$4))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/02/06 16:46

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1 **Prediction of distribution system disturbances**
Schwartzenberg, J.W.; Nwankpa, C.O.; Fischl, R.; Sundaram, A.;
Power Electronics Specialists Conference, PESC '94 Record., 25th Annual IEEE , June 1994
Page(s): 1077 -1082 vol.2

[Abstract] [PDF Full-Text (340 KB)] **IEEE CNF**

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Design and implementation of a series voltage sag compensator under practical utility conditions

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Abstract:
Voltage sags have become one of the most important power quality concerns recent years. According to survey results across the US, voltage sags and shc duration power outages account for 92% of power quality problems encounte industrial customers. Voltage sags often cause undervoltage faults in various sensitive loads and subsequently interrupt the manufacturing processes. Sucl interruptions often inflict severe losses for industries. In Taiwan, ROC, most h tech manufacturers use uninterruptible power supplies to avoid interruptions, the cost effectiveness of such an approach remains unclear. As the utility gric continues to improve the reliability of electric power, the inverter-based volta sag compensator has become a viable solution to prevent production interrup resulting from voltage sags. The existing sag compensation systems accompli fast response within a small fraction of a fundamental cycle by tracking the li voltages closely, and switch on the compensator whenever the voltage wavef deviate from the normal values. However, the utility voltages often contain transient spikes with amplitudes up to 200% resulting from switching of powe factor-correction capacitors, circuit breakers switchings, lightning strikes, and on. Such transient disturbances may trigger the sag compensator into operat its controller is very sensitive. The switching frequency of the sag compensat inverter is inadequate to compensate the narrow pulses of voltage spikes. Furthermore, the power semiconductor devices (like insulated gate bipolar transistors) of the inverter may also be damaged due to overvoltage by the s In this paper, a brief overview of power quality issues of a high-tech industry in Taiwan is provided to validate the need for ride-through technologies. A synchronous-reference-frame-based controller for the inverter-based voltage compensator is also presented. A sag detection mechanism is included in the controller for correct and prompt identification of voltage sags. Disturbances l voltage spikes are attenuated to avoid any false triggering of the compensatc overall system responds to voltage sags and restores the voltage back to bal

1.0 pu for critical loads within one-eighth to one-fourt- h of a cycle, which me requirement of industry standards like the SEMI-F47 standard. Simulation an laboratory test results are presented to verify the functionality of the propose system.

Index Terms:
circuit breakers compensation invertors power capacitors power factor correction p supply quality power system faults SEMI-F47 standard Taiwan circuit breakers swit false triggering avoidance high-tech manufacturers industrial customers insulated ga bipolar transistors inverter-based voltage sag compensator lightning strikes line volta tracking manufacturing processes interruption power quality problems power semico devices power-factor-correction capacitors ride-through technologies sag compensat sag compensator inverter sag detection mechanism sensitive loads series voltage sa compensator short-duration power outages switching frequency synchronous-referer frame-based controller transient disturbances transient spikes undervoltage faults uninterruptible power supplies utility conditions voltage spikes voltage waveforms

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